



SHAMBHUNATH  
INSTITUTE OF  
ENGINEERING  
AND  
TECHNOLOGY,  
ALLAHABAD  
(SIET)

Contact Persons and  
Registration In charge

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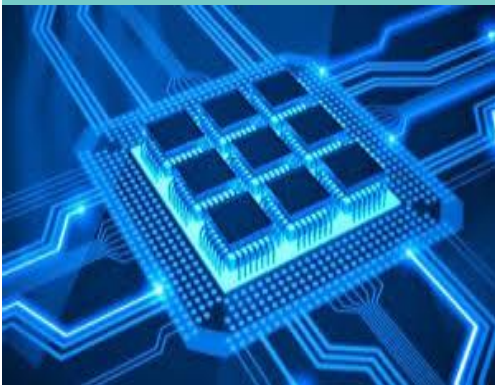
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Dr. A.P.J. Abdul Kalam Technical  
University, Lucknow

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President, UTTHAN,  
former member of planning  
commission, government of India,  
and vice-chairman state planning  
board Chhattisgarh,

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Dr. R.K. Singh  
Chief Administrative Officer, SGI

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Head-Department of ECE

Dr. Brijesh Mishra  
Assistant Professor

**FDP Coordinator**

Dr. Chandrabhan  
Dean Academics, SIET

**Organizers**

Prof. J.P. Mishra, Director R & D  
Dr. N.C. Agrawal, Professor  
Dr. K.P. Pandey, Associate Professor  
Dr. R.R. Tripathi, Associate Professor  
Mr. Kamal Mishra, Associate Professor

**Technical Education Quality  
Improvement Program III  
(TEQIP -III)**

Faculty Development Program  
On

“VLSI for Signal Processing  
and Communication”

16<sup>th</sup> – 20<sup>th</sup> July, 2018

Dr. A.P.J. Abdul Kalam Technical  
University, Lucknow



Organized by

Department of Electronics and  
Communication Engineering,  
Shambhunath Institute of Engineering and  
Technology, Allahabad

CAMPUS ADDRESS:

JHALWA, ALLAHABAD, UTTAR PRADESH,

## About SIET, Allahabad

SIET is an engineering institution of Allahabad, affiliated to Dr. A.P.J. Abdul Kalam Technical University, Lucknow, Uttar Pradesh, India. Admissions to the college are based upon the merit of the student in the State Entrance Exam (SEE) conducted by Dr. A.P.J. Abdul Kalam Technical University, Lucknow. It is an AICTE approved college. It is located in Jhalwa about 7 km away from Allahabad railway station and 1.5 km from the Airport. The college is governed by an NGO "UTTHAN" by Dr. D.N. Tewari, former member of planning commission, government of India, and vice-chairman state planning board Chhattisgarh. Dr. R.K. Singh is the C.A.O of the college. Former president Dr. A.P.J. Abdul Kalam visited the institute in 2009 and this shows the highly esteemed image of the college.

## About the Department

The department of Electronics and Communication in SIET Collage was started in year 2004. The Department possesses highest placement records and also secures 100% result in the university. The Electronics and communication Engineering department comprises of experienced faculty of different specializations of more than 10 years' experience. Department of ECE also runs M.Tech programme in VLSI Design.

## About FDP and its Objective

FDP is organized by the department of ECE, SIET, Allahabad under the aegis of TEQIP-III programme supported by Dr. A.P.J. A.K.T.U, Lucknow. The FDP "VLSI For Signal Processing and Communication" targets a goal of imparting a conceptualized knowledge about VLSI and communication systems useful in modern technology. It also includes hands on session on VLSI system, signal processing and communication.

## Major Topics that would be covered Under the FDP program

1. Semiconductor Physics
2. VLSI Devices Modeling Techniques
3. Low Power Flexible Devices
4. Microelectronics and Nano Materials
5. Advanced Spacecraft Navigation System
6. Modern Physics of Materials
7. 3-Dimensional Signal Processing
8. Satellite Communication

## Who can apply

The FDP program is open to all the faculties and aspirants of the above mentioned fields from different engineering colleges, Universities and technical institutes.

## Registration

There is no registration charge. The participants should register themselves by filing the registration form and email the scanned copy of the filled and duly signed registration form to the email [fdpsiet@gmail.com](mailto:fdpsiet@gmail.com) on or before 12<sup>th</sup> July, 2018. The last date of intimation to the applicants by the department is 14<sup>th</sup> July, 2018. The hard copy of the registration form must be submitted on the first day of the program on the registration desk.

## Travel and Accommodation

Participants will have to make their own arrangements for their travel. Stay & dinner of participants from outside Allahabad will be arranged in college campus on first come first serve basis, as per institute hostel/guest house capacity.

## Resource Persons

Resource persons to conduct the program are expert from technical institutions (IIT's, NIT's, IIIT's and Universities) and industries (ISRO and DRDO) with excellent record of accomplishments in their fields of research and education sector.

Name of eminent speakers are:

1. Dr. S.G. Prakash, Professor, AU
2. Dr. Sudarshan Tiwari, Professor MNNIT Allahabad
3. Dr. Haranath Kar, Professor, MNNIT Allahabad
4. Dr. R.A. Mishra, Professor, MNNIT Allahabad
5. Dr. V.K. Srivastava, Professor, MNNIT Allahabad
6. Dr. Manish Goswami, Asso. Professor, IITA
7. Dr. Rajneesh Srivastava, Asso. Professor, AU
8. Dr. Somak Bhattacharyya, Asst. Professor, IIT-BHU
9. Dr. D.K. Mishra, Scientist, ISRO, India
10. Dr. Sitangshu Bhattacharya, Asst. Professor, IITA
11. Mr. Gaurish Tripathi, Scientist, DRDO, India
12. Dr. Sunny Sharma, Asst. Professor, IIIT Allahabad.



**One Week Faculty Development Programme  
On  
VLSI for Signal Processing and Communication  
July 16<sup>th</sup> – 20<sup>th</sup>, 2018**

*Organized by*  
**Department of Electronics and Communication**  
**Shambhunath Institute of Engineering & Technology, Allahabad**  
*under the support of*  
**TEQIP-III**  
**Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow**

**Registration Form**

1. Name of Candidate: \_\_\_\_\_
2. Father's Name: \_\_\_\_\_
3. Date of Birth: \_\_\_\_\_
4. Designation: \_\_\_\_\_
5. Name of the Institute: \_\_\_\_\_
6. Address for Correspondence: \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_
7. E-mail Address: \_\_\_\_\_ Mobile No.: \_\_\_\_\_
8. Do you need accommodation? (Yes / No): \_\_\_\_\_

**Declaration by the Candidate**

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing the FDP under TEQIP-III. If selected, I undertake to attend the FDP for the entire duration, otherwise I will not be eligible for the issuance award of participation. I also undertake the responsibility to inform the Coordinator in advance in case of inability to attend the programme so that if seats are available, other applicant may be considered in my place.

Date: \_\_\_\_\_

Signature of Applicant

Place: \_\_\_\_\_

**Declaration by the Head of Institution**

Mr./Ms./ Dr. \_\_\_\_\_ is working as \_\_\_\_\_ in our Institution and is hereby allow him/her for the FDP. He/ She will be permitted to attend the programme for entire duration of FDP, if selected.

E-mail: \_\_\_\_\_

Mobile: \_\_\_\_\_

**Seal & Signature of Sponsoring Authority**